

ABSTRACT OF THE DISCLOSURE

When data recording is performed on an optical disk provided with a data recording track wobbled by a wobble signal having predetermined frequency components, a recording clock generator circuit extracts the wobble signal, generates a divided clock signal obtained by dividing the frequency of a recording clock signal at a frequency dividing rate that is set through predetermined procedures and is different from a reference frequency dividing rate, generates a phase difference signal as a result of a comparison between the phase of the wobble signal and the phase of the divided clock signal, generates a frequency control signal obtained based on the phase difference signal, and then generates a recording clock signal having a frequency controlled in accordance with the frequency control signal.

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